



RoodMicrotec
powerful solutions



RoodMicrotec Newsletter

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**Special
edition**

RoodMicrotec seminar

‘The seminar to be held on 21 October is the result of discussions with a number of RoodMicrotec’s customer groups, such as OEMs, IDMs and fabless design companies,’ explains Dieter Schreiber of RoodMicrotec. ‘The seminar focuses on the growing requirements of the industry such as ‘zero-defect’, in-time development and delivery, and last but not least cost pressure from the market. We have composed an interesting programme aimed at decision-makers in fabless design companies and design departments of OEM companies, and at semiconductor professionals like product engineers, quality engineers and/or test engineers.’

October 21, 2010

9 am to 5:30 pm

RoodMicrotec seminar:
Solutions for reliable, timely and cost-effective results.
Nördlingen, Germany.

Robert Kraus

After welcoming words and an introduction by Philip Nijenhuis, our CEO, Robert Kraus, managing director of Inova, Germany, will briefly go into the reason for the meeting.

Inova Semiconductors GmbH is a fabless semiconductor manufacturer headquartered in Munich, Germany. The company was

founded in 1999 and specialises in the development of state-of-the-art products for Gigabit/s serial data communication.

The products are manufactured in leading factories in Europe and Asia and sold through a global distribution network.

Peter Jacob

Professor Peter Jacob of EMPA in Switzerland will give information on the (well-established) FIB services and more specifically on in-time development by FIB chip modification. In the same session Florian Hauf, our FIB specialist, will explain how to use FIB services. Because of the increasing design complexity it is becoming harder to realise “first time right” designs. Our FIB services are an important tool to achieve this.

EMPA is an interdisciplinary research and services institute for material sciences and

technology development. Its research and development activities focus on meeting

the requirements of industry and the needs of society.

Eckard Schöller

In his presentation, Eckard Schöller, Opto/Mechanical Qualification manager at RoodMicrotec, will describe quality issues with board level interconnects and multi-die stacking technologies, temperature profiles during production and ways to qualify as well as failure modes.

Eckard Schöller gained extensive experience with printed circuit board (PCB) qualification at Alcatel.

Helmut Keller

Helmut Keller, managing director of Consulting Engineering Services in Germany and Chairman Europe of SAE International Automotive Electronics Reliability Committee, will elaborate on reliable products by 'robustness validation'. The AEC-Q (Automotive Electronics Council) standards dating from the sixties have become obsolete in times when zero-defect is the standard. Robustness validation is a method to optimise safety margins conforming to today's requirements.

SAE International is a global association of more than 128,000 engineers and related technical experts in the aerospace, automotive and commercial vehicle industries. SAE International's core competencies are life-long learning and voluntary consensus standards development.

Heinrich Theodor Vierhaus

Professor Heinrich Theodor Vierhaus, head of 'technical informatic' at the Cottbus Technical University in Germany, is a well-known expert in the software and design world. To reduce test costs, it is important to offer options to allow next generation chips to be tested on existing test platforms. The subject of his presentation is how to design for testability or 'design-for-test' (dft).

Cottbus Technical University was founded in 1991. The university currently has some 6,400 students and 1218 employees. Cottbus, the economic, science and technology centre in the south of Brandenburg, offers outstanding conditions and requirements for promising investments and development opportunities.

Thorsten Bucksch

In an interview in one of our newsletters RoodMicrotec CTO Thorsten Bucksch said: 'It is essential, and at the same time one of our strong points, that we are able to reduce the overall test time without losing test coverage. It is hard for us to beat our Asian competitors purely on cost, but this gives us a competitive edge.' In this presentation Thorsten Bucksch will further discuss economic test platform selection while retaining zero-defect.

Bruno Benedetti

As a RoodMicrotec customer Bruno Benedetti, EMEA supply chain design manager at Landis+Gyr in Switzerland, will explain his reasons for outsourcing supply chain management activities and tell about his experience with outsourcing activities to RoodMicrotec.

Landis+Gyr helps utilities manage energy better. As the biggest global player in electricity metering with one of the broadest products and services portfolios in the industry, its meters and solutions empower utilities and consumers to improve their energy efficiency, reduce their energy costs and thus contribute to a sustainable use of resources.

Paul Jamie

Paul Jamie from the National Microelectronic Institute (NMI) in the UK will describe the relation between NMI and the UK semiconductor market.

The National Microelectronics Institute (NMI) is the premier trade association representing the semiconductor industry in the UK and Ireland. Its objective is to help build and support a strong micro and nanoelec-

tronics community by acting as a catalyst and facilitator for both commercial and technological development. A not-for-profit organisation funded by its members, the NMI's membership spans

the supply chain and includes fabless semiconductor manufacturers, IDMs, foundries, design services, IP providers, business associates, and research and academic institutions.



Throughout the day we offer guided tours of the site, during which you will get an impression of our equipment and capabilities. One of the highlights will be the monitoring (measuring) Burn-In system.

We look forward to meeting you at the seminar.

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